

University of Al Maarif Department of Medical Instruments Techniques Engineering Class: 2nd



**Digital Electronics** 

**MIET2203** 

Lecture 6: Karnaugh map



Lecturer: Mohammed Jabal 2024 - 2025

## Karnaugh map

•A Karnaugh map provides a pictorial method of grouping together expressions with common factors and therefore eliminating unwanted variables. The Karnaugh map can also be described as a special arrangement of a truth table. The diagram below illustrates the correspondence between the Karnaugh map and the truth table for the general case of a two-variable problem.



Truth Table.

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## Karnaugh map

• The values around the edge of the map can be thought of as coordinates. right-hand as an example, the square on the top right-hand corner of the map in the above diagram has coordinates A=1 and B=0. This square corresponds to the row in the truth table where A=1 and B=0 and F=1. Note that the value in the F column represents a particular function to which the Karnaugh map corresponds.

• The number of cells in a Karnaugh map, as well as the number of rows in a Truth table, is equal to the (total number of possible input variable combinations). For three variables, the number of cells is  $2^3 = 8$ . For four variables, the number of cells is  $2^4 = 16$ .

# Karnaugh map

### Example:





The process that results in an expression containing the fewest possible terms with the fewest possible variables is called **minimization**. After an SOP expression has been mapped, a minimum SOP expression is obtained by grouping the 1s and determining the minimum SOP expression from the map.

### **Grouping the 1s**

You can group 1s on the Karnaugh map according to the following rules by enclosing those adjacent cells containing 1s. The goal is to maximize the size of the groups and to minimize the number of groups.

- 1. A group must contain either 1, 2, 4, 8, or 16 cells, which are all powers of two. In the case of a 3-variable map,  $2^3 = 8$  cells is the maximum group.
- 2. Each cell in a group must be adjacent to one or more cells in that same group, but all cells in the group do not have to be adjacent to each other.
- **3.** Always include the largest possible number of 1s in a group in accordance with rule 1.
- **4.** Each 1 on the map must be included in at least one group. The 1s already in a group can be included in another group as long as the overlapping groups include noncommon 1s.

#### EXAMPLE 4-27

Group the 1s in each of the Karnaugh maps in Figure 4–33.



**FIGURE 4–33** 

#### Solution

The groupings are shown in Figure 4–34. In some cases, there may be more than one way to group the 1s to form maximum groupings.



FIGURE 4–34











 $\overline{D} + A\overline{B}C + B\overline{C}$ 







 $\overline{D} + A\overline{B}C + B\overline{C}$ 

B	Α	X
0	0	1
0	1	0
1	0	0
1	1	1

$$\begin{array}{ccc} \overline{B} & B \\ \overline{A} & 1 & 0 \\ A & 0 & 1 \end{array} X = \overline{A}\overline{B} + AB$$



![](_page_12_Figure_1.jpeg)

![](_page_12_Figure_2.jpeg)

### Don't care

• Some logic circuits can be designed so that there are certain input conditions for which there are no specified output levels, usually because these input conditions will never occur. In other words, there will be certain combinations of input levels where we "don't care" weather the output is HIGH or LOW. This is illustrated in the following truth table.

![](_page_13_Figure_2.jpeg)

## References

[1] Digital fundamentals / Thomas L. Floyd. —Eleventh edition.

[2]