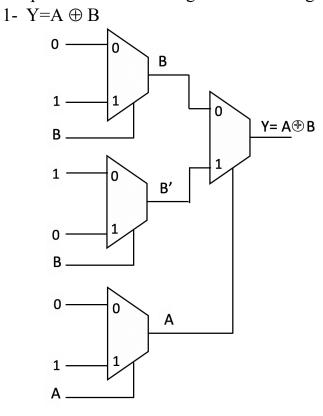
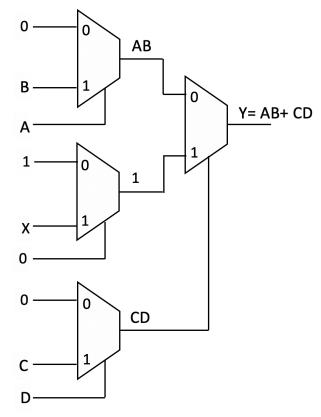
Advanced Digital Electronics

Computer engineering techniques

Q2: implement the following functions using MUX-based logic blocks







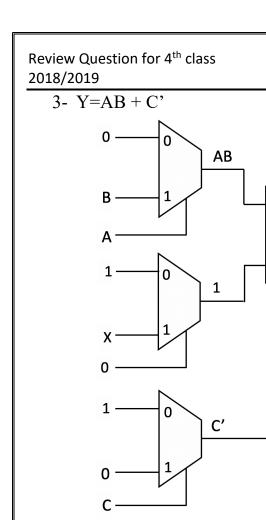
Advanced Digital Electronics

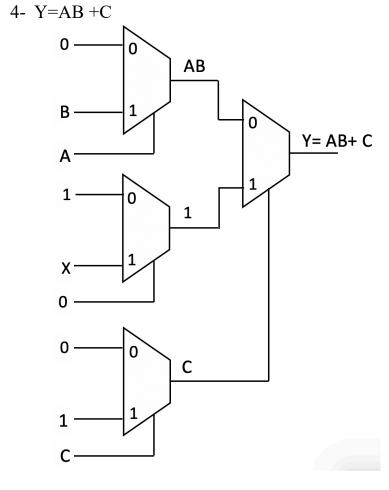
Y = AB + C'

0

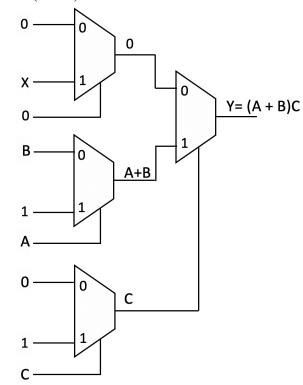
1

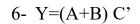
Computer engineering techniques

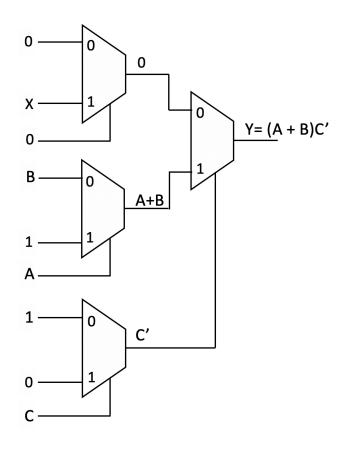


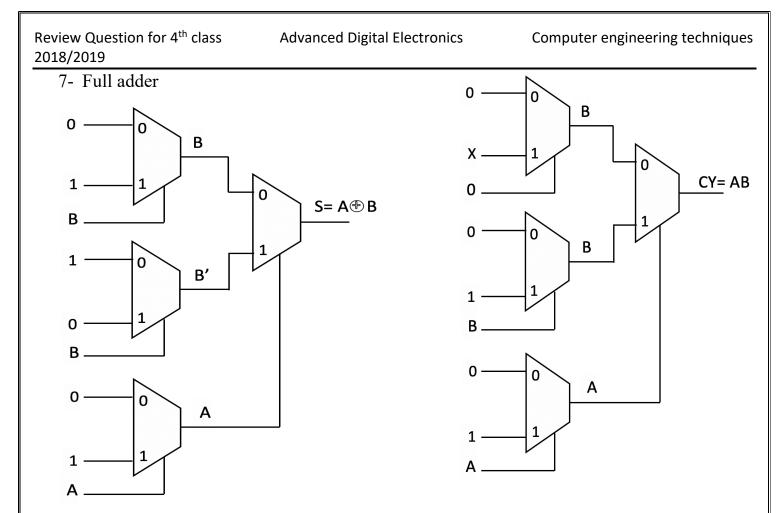


5- Y=(A+B) C



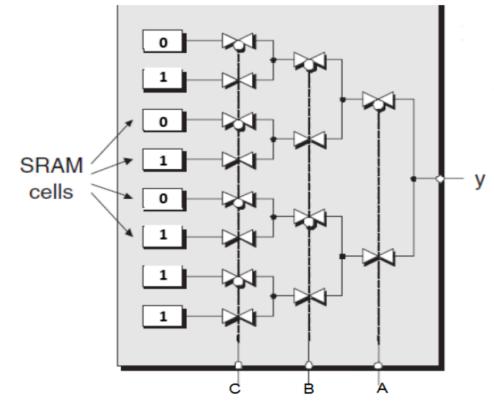






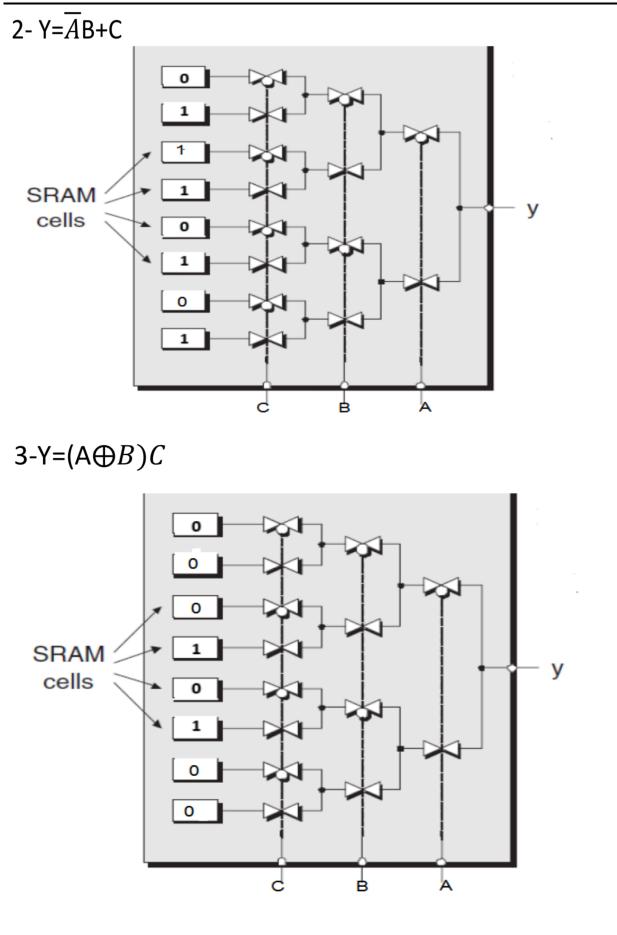
Q3: implement the following functions using LUT-based logic blocks:

1-Y=AB+C

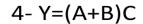


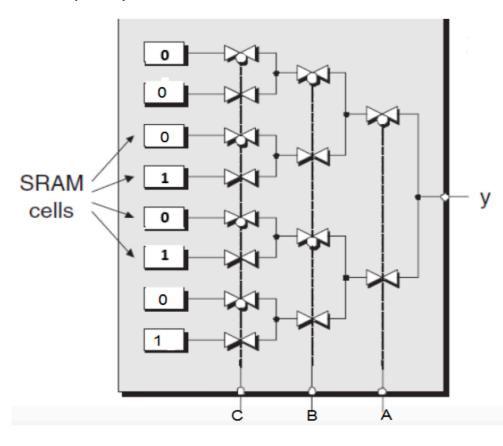
Review Question for 4th class 2018/2019

Advanced Digital Electronics



Review Question for 4th class 2018/2019





Q4: design the following functions by using PLA type of SPLDs $f_1 = x_1 x_2 + x_1 \overline{x_3} + \overline{x_1} \overline{x_2} x_3$ $f_2 = x_1 x_2 + \overline{x_1} \overline{x_2} x_3 + x_1 x_3$

